

Appln No. 10/727,227  
Amdt. Dated August 22, 2006  
Response to Final Office Action of July 21, 2006

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### **REMARKS/ARGUMENTS**

In response to the Examiner's final Office Action of July 21, 2006 the Applicant respectfully submits the accompanying Amendment to the claims and the below Remarks.

#### ***Regarding Amendment***

In the Amendment:

independent claim 1 is amended to be directed to a printhead integrated circuit, to clarify that the first and second reticle exposed areas are sequentially arranged, that the first areas having inkjet nozzle logic circuitry, that the second areas have connection pads for connecting power and data to the nozzle logic, and to specify that the ends of the first and second areas are configured to be interconnectable in the manner claimed. Support for these amendments can be found, for example, at paragraphs [4243]-[4355] and [4367]-[4370] of the present specification;

withdrawn claims 7-15 are cancelled;

dependent claims 16-19 are cancelled in accordance with amended claim 1; and

dependent claims 20-24 are unchanged.

It is respectfully submitted that the above amendments do not add new matter to the present application, nor any new issues to the prosecution of the present application.

#### ***Regarding 35 USC 112, first paragraph Rejections***

It is respectfully submitted that above-discussed amended claim 1 complies with both the written description and enablement requirements, for at least the following reasons.

In response to the Examiner's assertions in the current final Office Action, claim 1 has been amended as discussed above so as to:

be directed to a printhead integrated circuit, which is illustrated, for example, in Figs. 254, 269, 280 and 295-300 and described at paragraphs [4243]-[4294] of the present application, such that one of ordinary skill in the art would understand the final CMOS/MEMS structure embodied by amended claim 1 and be able to ascertain the final claimed integrated circuit structure once the reticle layout is removed;

recite that the first reticle exposed areas have inkjet nozzle logic circuitry and the second reticle exposed areas have pads for connecting power and data to the nozzle logic, which is illustrated, for example, in Figs. 308, 323 and 324 and described at paragraphs [4295]-[4355] and [4367]-[4370] of the present application, such that one of ordinary skill in the art would understand the metes and bounds of the claimed invention; and

recite that the first/second ends of the first areas are configured to be interconnectable with either the second/first ends of the first areas or the first/second ends of the second areas, as is illustrated, for example, in Figs. 323 and 324 and described at paragraphs [4295]-[4355] and [4367]-[4370] of the present application, such that one of ordinary skill in the art would understand that "stitch" and "overlapping" mean this interconnection of the reticle areas of the printhead integrated circuits of the described bilithic printhead and would understand which are the "top" and "bottom" of each respective area (or first and second ends as claimed) from Fig. 324 and paragraphs [4369] and [4370] of the present application.

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***Regarding 35 USC 112, second paragraph Rejections***

It is respectfully submitted that above-discussed amended claim 1 clearly points out what is included or excluded by the claim language, for at least the following reasons.

In response to the Examiner's assertions in the current final Office Action, claim 1 has been amended as discussed above so as to:

be directed to a printhead integrated circuit, which is illustrated, for example, in Figs. 254, 269, 280 and 295-300 and described at paragraphs [4243]-[4294] of the present application, such that one of ordinary skill in the art would understand the final CMOS/MEMS structure embodied by amended claim 1; and

recite that the first reticle exposed areas have inkjet nozzle logic circuitry and the second reticle exposed areas have pads for connecting power and data to the nozzle logic, which is illustrated, for example, in Figs. 308, 323 and 324 and described at paragraphs [4295]-[4355] and [4367]-[4370] of the present application, such that one of ordinary skill in the art would understand the metes and bounds of the claimed invention.

***Regarding 35 USC 102(e) Rejections***

It is respectfully submitted that the subject matter of above-discussed amended independent claim 1 is not disclosed by Jarrod et al. (US 2002/0093548), for at least the following reasons.

As discussed above, independent claim 1 has been amended to clearly recite that the first and second reticle exposed areas are sequentially arranged to form the printhead integrated circuit and that the first/second ends of the first areas are configured to be interconnectable with either the second/first ends of the first areas or the first/second ends of the second areas (see Figs. 323 and 324 and paragraphs [4295]-[4355] and [4367]-[4370] of the present application).

On the other hand, Jarrod discloses forming the printhead 10 by arranging the array of inkjet devices 12 on the substrate 13 so that the electrodes 30,32 of each inkjet device are individually addressed at the edges of the substrate, as illustrated in Figs. 1 and 3 of Jarrod (see also paragraphs [0022]-[0024] of Jarrod). Thus, Jarrod clearly does not disclose, or suggest, the arrangement of the claimed invention.

Therefore, the subject matter of amended independent claim 1, and claims 20-24 dependent therefrom, is not disclosed or suggested by Jarrod.

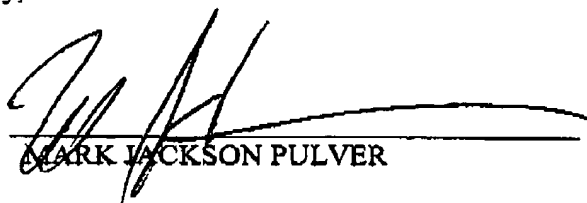
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It is respectfully submitted that all of the Examiner's rejections have been traversed. Accordingly, it is submitted that the present application is in condition for allowance and reconsideration of the present application is respectfully requested.

Very respectfully,

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